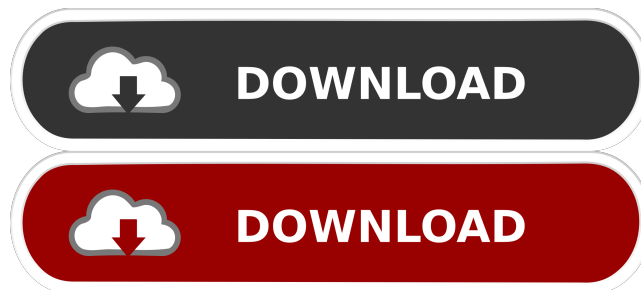

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I Samne la de dassolo maglio fpt akti abus ca1. Field of the Invention The present invention relates to a method for forming a roughness pattern on a substrate. 2. Description of the Related Art In recent years, an apparatus for forming an ultrafine wiring pattern on a substrate, such as a semiconductor substrate, is increasingly required to increase the number of wiring patterns formed on the substrate and to narrow the spaces between wiring patterns. Such a pattern forming apparatus often performs coating of a resist material to form a resist film, and exposure and development of the resist film in order to form a resist pattern. The pattern forming apparatus is required to provide an improved throughput. In a conventional exposure apparatus, a reticle plate is mounted to an exposure table. A reticle pattern formed on the reticle plate is transferred to an exposure area on a wafer through a projection optical system. When a resist pattern is formed on a wafer, the wafer is mounted to a stage, and a position of the wafer is adjusted on the stage by measuring the position of the wafer with a measurement device, such as an interferometer, so that the position of the wafer agrees with a position of a pattern forming area on the wafer. In recent years, for example, it is required that a resist pattern be formed on a wafer in a micro size. In order to form a resist pattern of a micro size, a reticle pattern formed in an exposure apparatus is required to have a pattern size that is smaller than the exposure apparatus. Therefore, a high resolution reticle pattern is required. In the conventional exposure apparatus, when

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